

Notice of Allowability	Application No.	Applicant(s)
	10/719,893	NISHIMURA, MOTOAKI
	Examiner	Art Unit
	Amare Mengistu	2629

-- **The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to Nov.21,2003.
2. The allowed claim(s) is/are 1-20.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 4/22/05
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.



Amare Mengistu
Primary Examiner

DETAILED ACTION

Allowable Subject Matter

1. Claims 1-20 are allowed.
2. The following is an examiner's statement of reasons for allowance: the prior arts has failed to discloses applicant's claimed invention "*A power source circuit comprising: a power source wiring at a high potential side provided with a first power source voltage and a second power source voltage; a power source wiring at a low potential side; a booster circuit as a charge pump installed between the power source wiring at a high potential side and the power source wiring at a low potential side and provided with a plurality of switching transistors and a plurality of capacitors; and means for controlling the booster circuit, wherein a predetermined number of power sources including the power source wiring at the high potential side are further provided with the power source circuit, wherein an input voltage is selectively input to a part of the booster circuit from any one of the predetermined number of power sources, the means for controlling fixes all of the switching transistors inputting the predetermined number of power sources except one of the switching transistors; and wherein the means for controlling controls the booster circuit to implement a boosting operation by switching the switching transistor not fixed with a predetermined frequency and a predetermined phase relationship so as to switch the input of the predetermined number of power sources and selectively output a predetermined number of output voltages from the booster circuit.*" And "*a power source wiring at a high potential side provided with a first power*

source voltage and a second power source voltage; a power source wiring at a low potential side; a multi stage booster circuit as a charge pump installed between the power source wiring at a high potential side and the power source wiring at a low potential side and provided with a plurality of switching transistors and a plurality of capacitors to generate an output voltage, which are multistage-boosted based on the first power source voltage or the second power source voltage at the high potential side; and means for controlling that controls the booster circuit to implement a boosting operation by switching the plurality of the switching transistors in the booster circuit with a predetermined frequency and a predetermined phase relationship, wherein a number of multiple stages of the multi stage booster circuit decreases by short circuiting an output terminal of the multi stage booster circuit with a output terminal of a capacitor at least constituting a final stage of the multi stage booster circuit and removing the capacitor."

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Amare Mengistu whose telephone number is (571) 272-7674. The examiner can normally be reached on M-F,T-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on (571) 272-3639. The fax phone

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number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Amare Mengistu
Primary Examiner
Art Unit 2629

AM

April 14, 2006